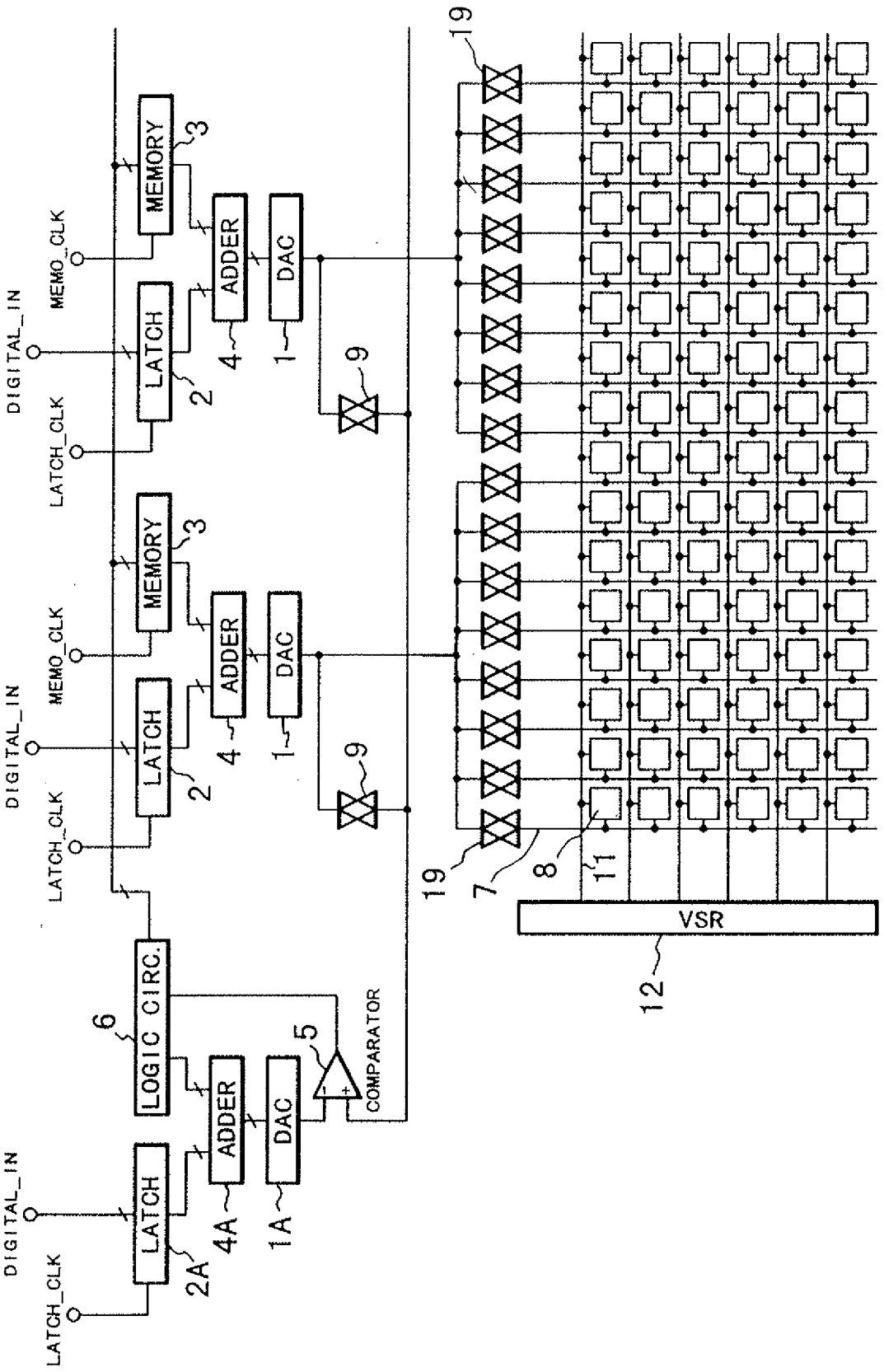
DIGITAL_IN
LATCH_CLK
MEMO_CLK
LATCH
MEMORY
ADDER
DAC
LOGIC CIRC.
COMPARATOR
VSR
2A
4A
1A
6
5
2
3
4
1
9
19
7
8
11
12

FIG. 2